

FIG. 1

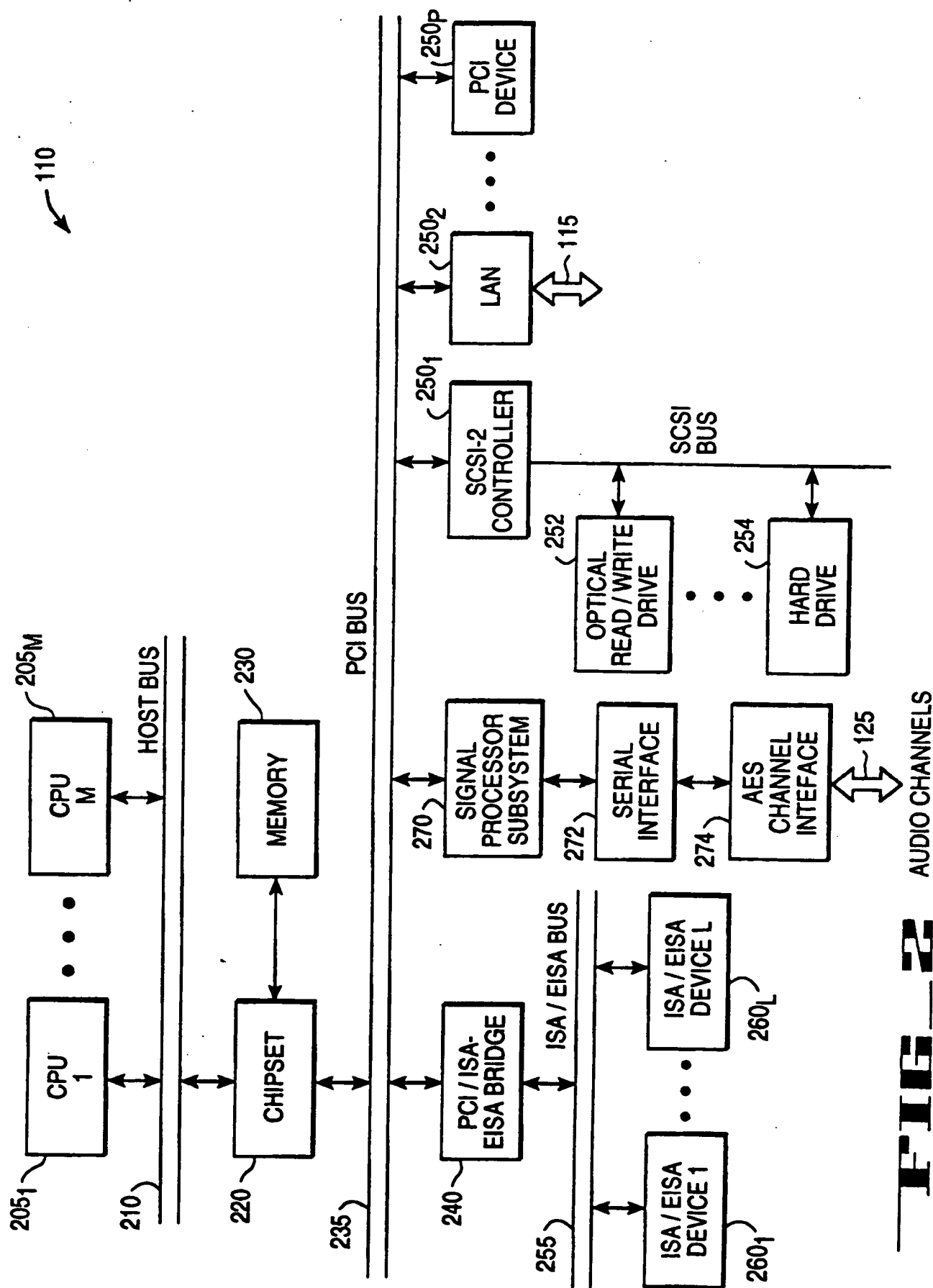


FIG. 2

SOFTWARE ARCHITECTURE

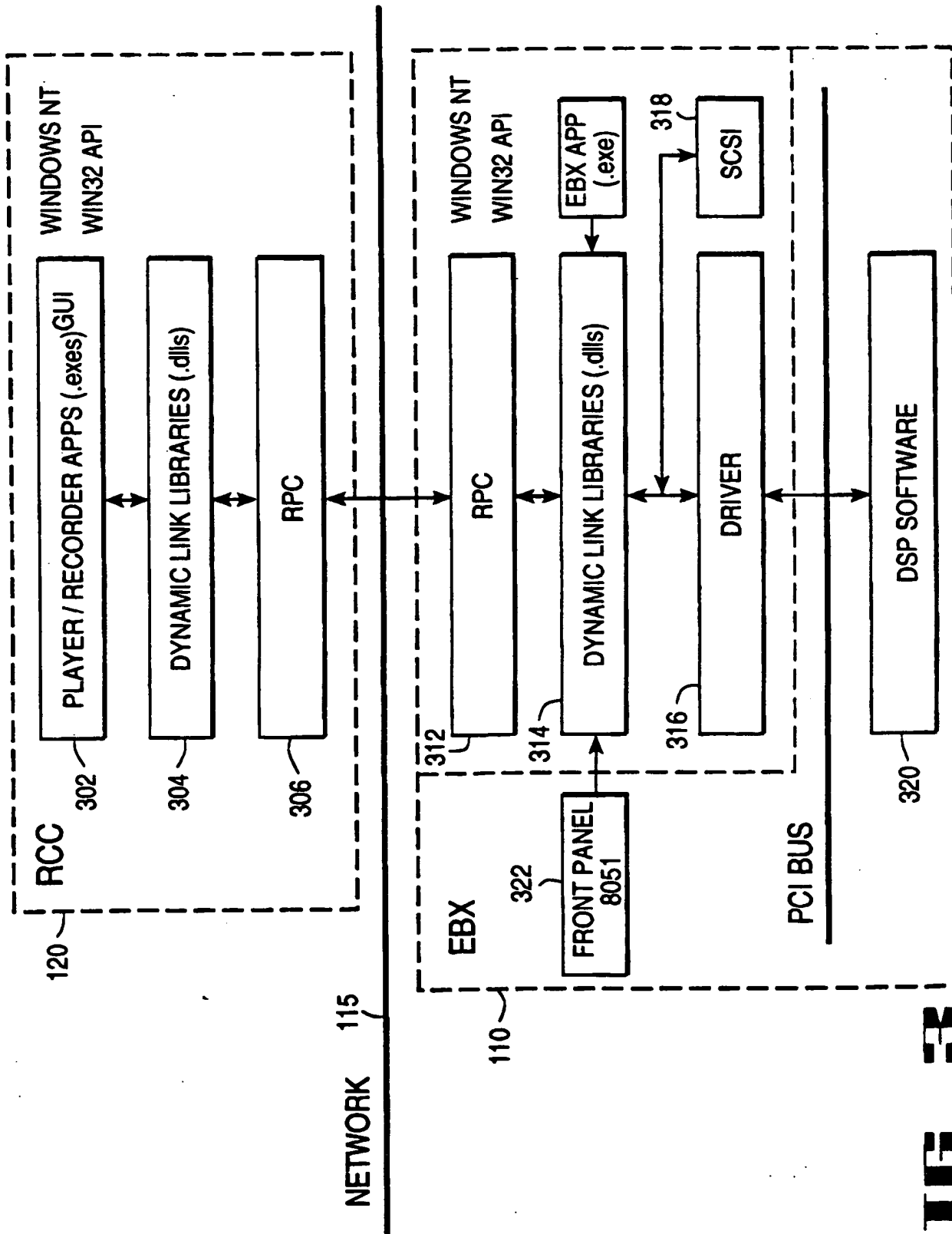


FIG 3

264260" 80/9E680

OPR

Project Options Help

A→A

Recorders

412

414 416 418 420 422 424 426 428

Mach, Trk ID	Track Name	s	R	I	S	M	Gain	Offset	Mach, Trk ID	Track Name	s	R	I
105_1	Track 1						0 dB	00:00:00	106_2	Track 2			
105_2	Track 2						0 dB	00:00:00	106_3	Track 3			
105_3	Track 3						0 dB	00:00:00	106_4	Track 4			
105_4	Track 4						0 dB	00:00:00	106_5	Track 5			
105_5	Track 5						0 dB	00:00:00	106_6	Track 6			
105_6	Track 6						0 dB	00:00:00	106_7	Track 7			
105_7	Track 7						0 dB	00:00:00	106_8	Track 8			
105_8	Track 8						0 dB	00:00:00					
106_1	Track 1						0 dB	00:00:00					

Show All Rows

1 2 3 4 5 6 7 8

All None

432

434 436 438

00:00:00:00:00

<< < > >>

Commit

442

444 446 448 450 452 454

Master Slave Local Online Offline

<< < > >>

456

458

460

462

464

466

468

470

472 In 474 Out

Save In Save Both

00:00:00:00:00 00:00:00:00:00

Project

AES Reference \AES3

Sync Bus 1

Time Remaining [1 Track] 02:53:36

Figure 4

400

Figure 5